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**SUBMITTED BY:**

**UQBA GULZAR (067)**

**ZUNAIRA NOOR (075)**

**ZUNAIRA KHATOON (074)**

**HADIA ZAKIR (019)**

**SUBMITTED TO:**

**Engr. AHSEN ILYAS**

**COURSE TITLE:**

**CALD LAB**

**DEPARTMENT:**

**SOFTWARE ENGINEERING**

**SECTION:**

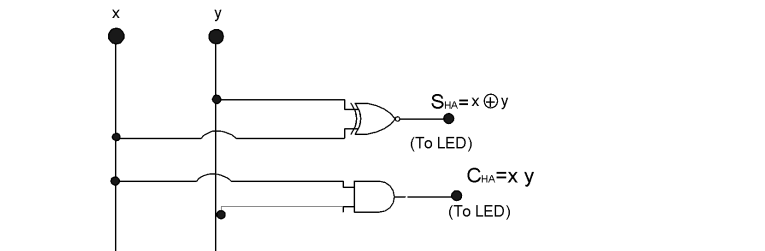
**BSE II-B**

**LAB # 6**

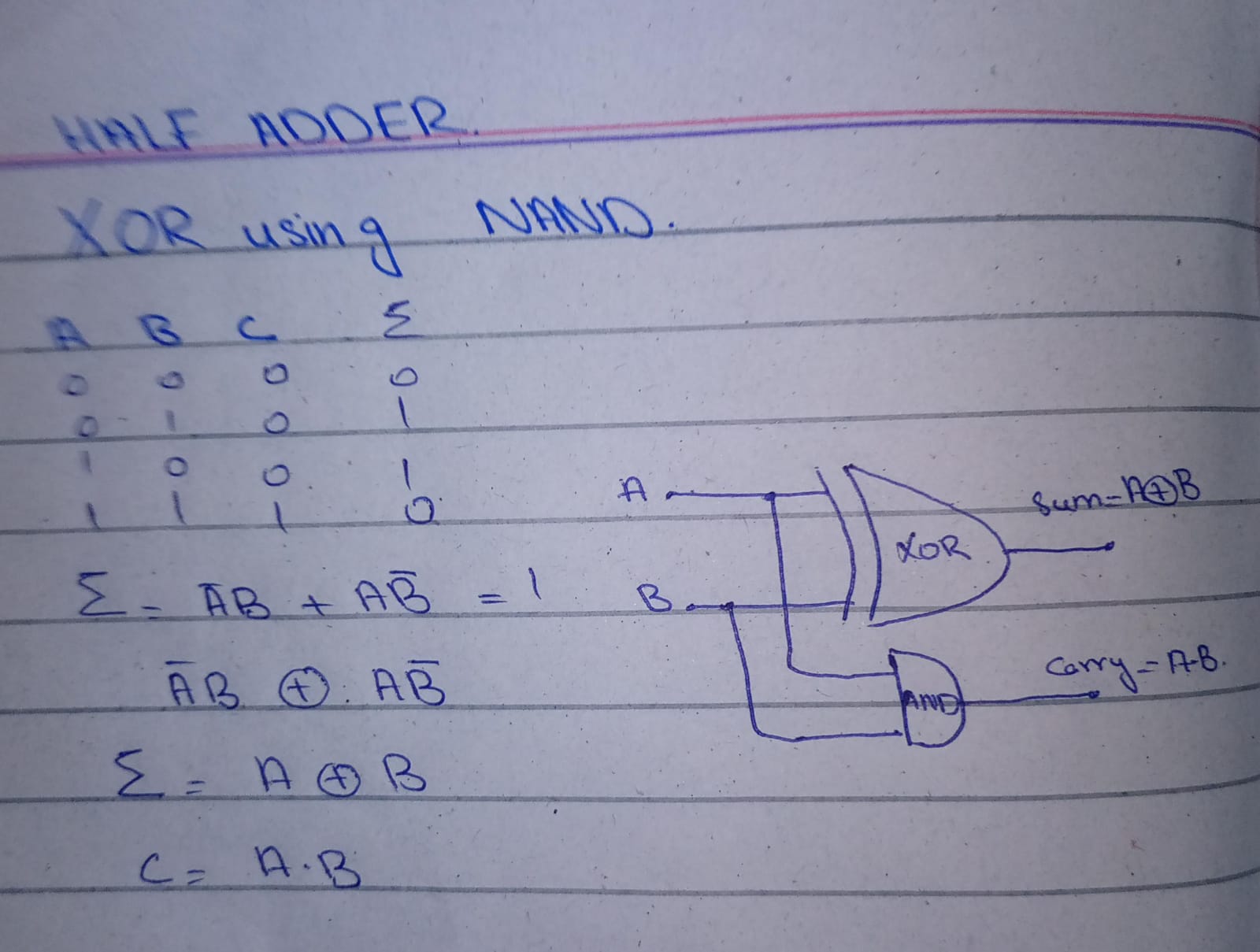
**IMPLEMENTATION OF HALF ADDER AND FULL ADDER**

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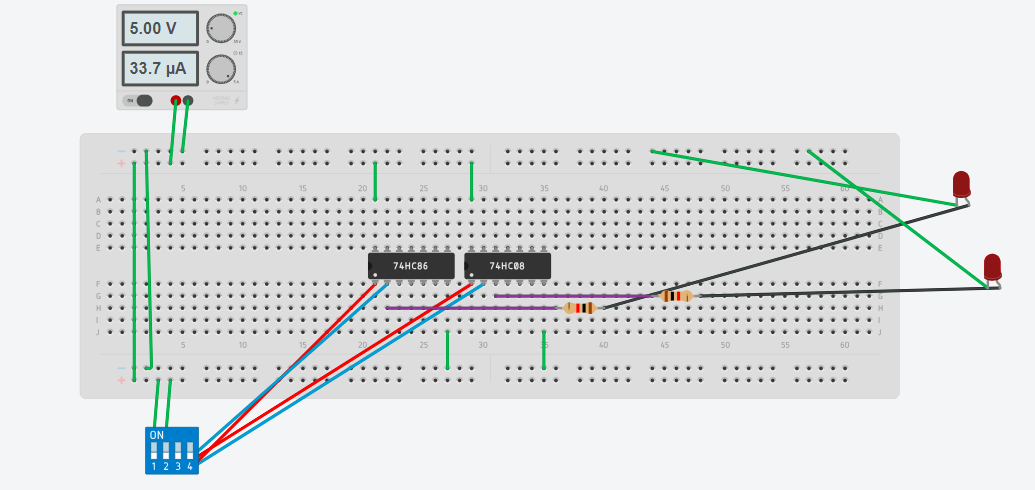
* **Half Adder: Logic diagram:**



**Truth Table:**

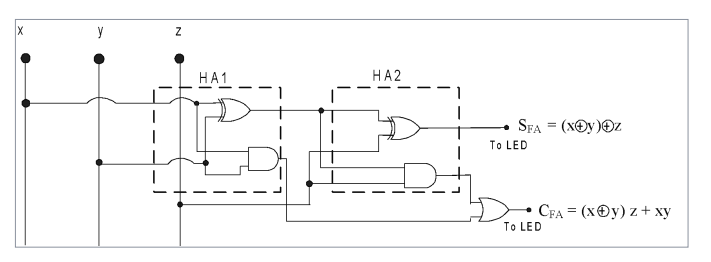


**Implementation of Half adder:**

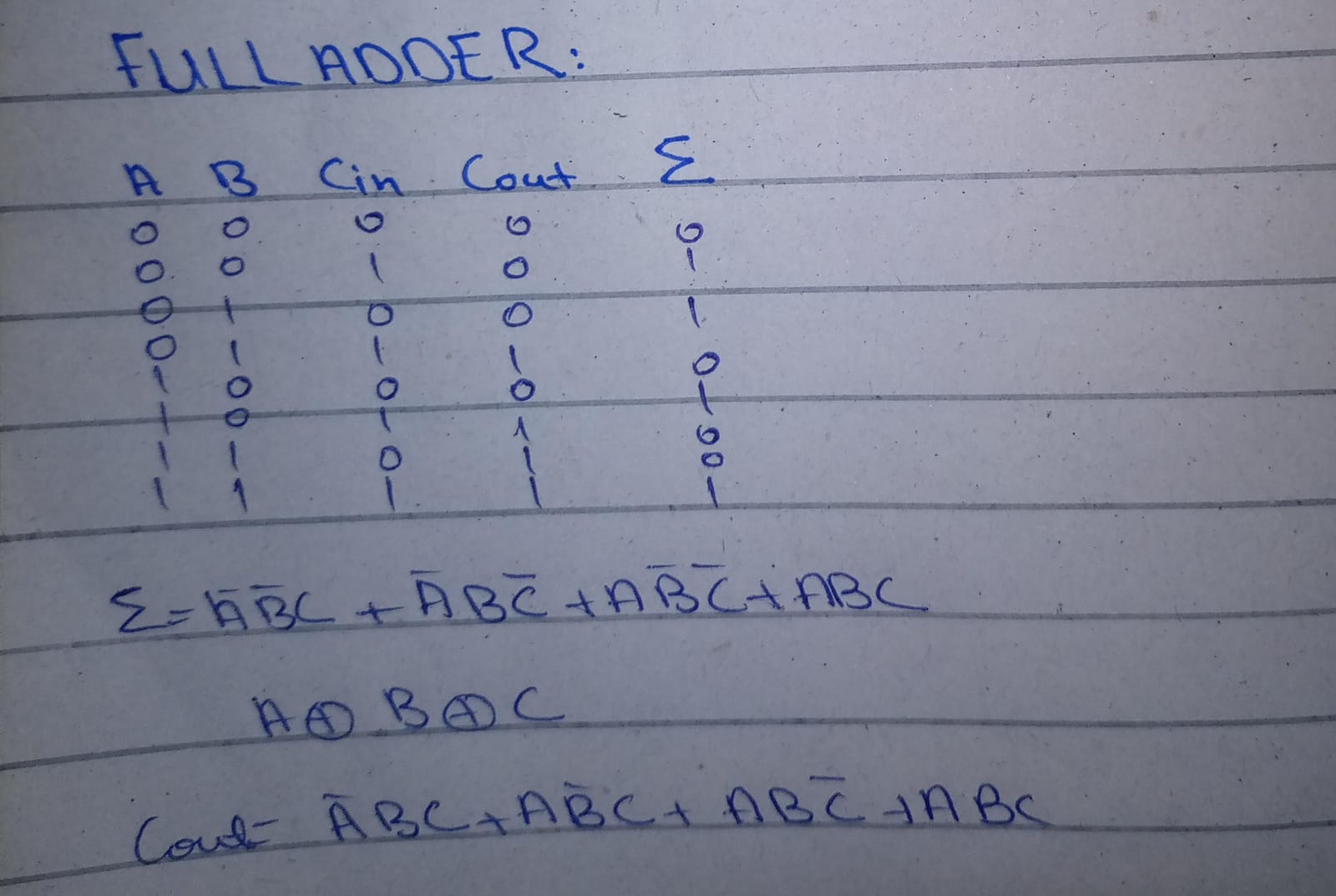
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* **Full Adder:**

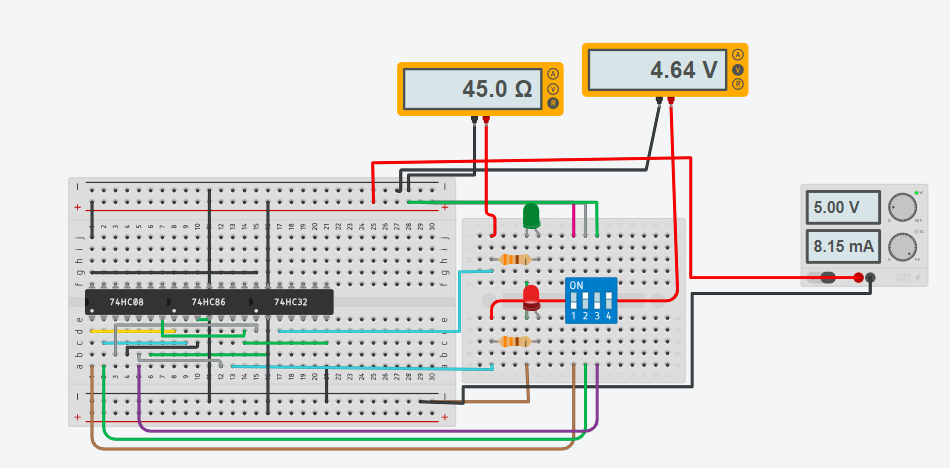
**Logic diagram:**



**Truth Table:**

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**Circuit Diagram:**

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